

FIG. 5

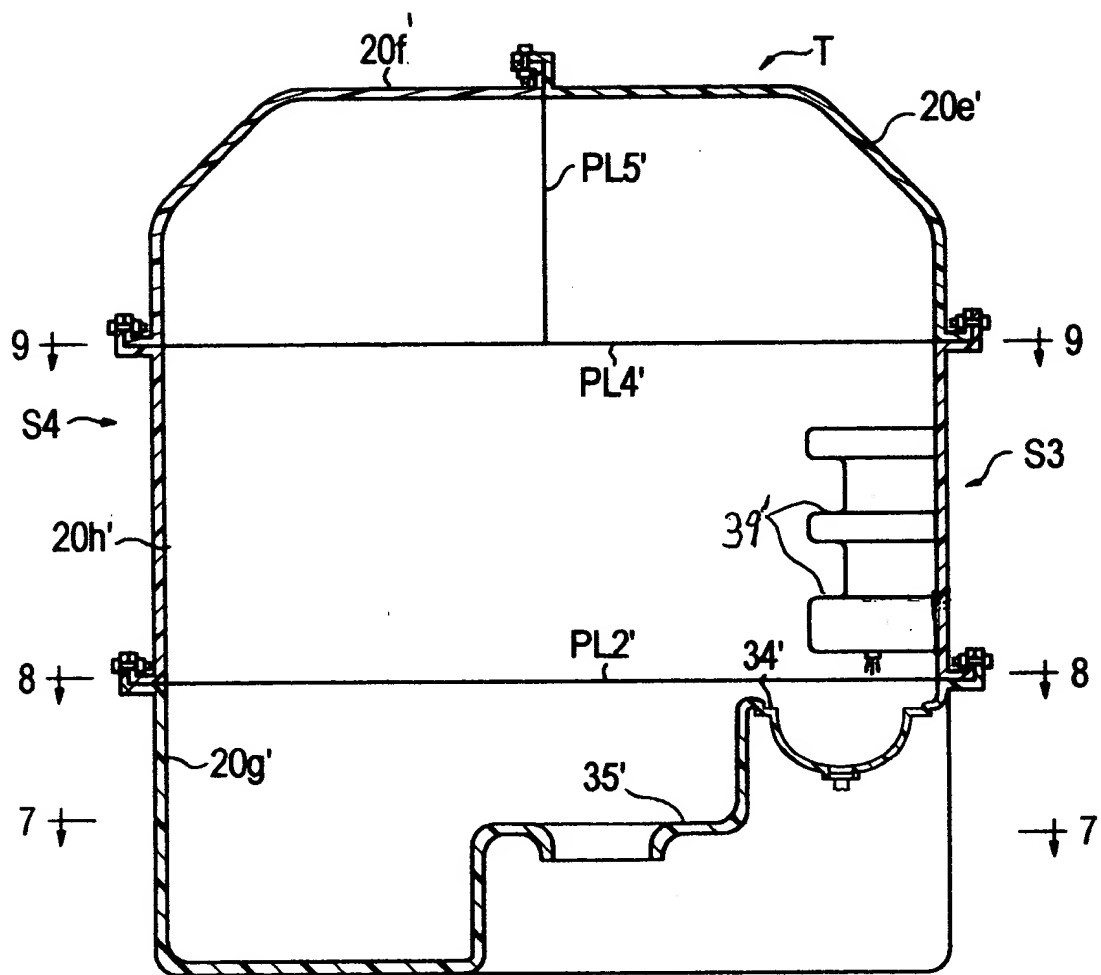


FIG. 6



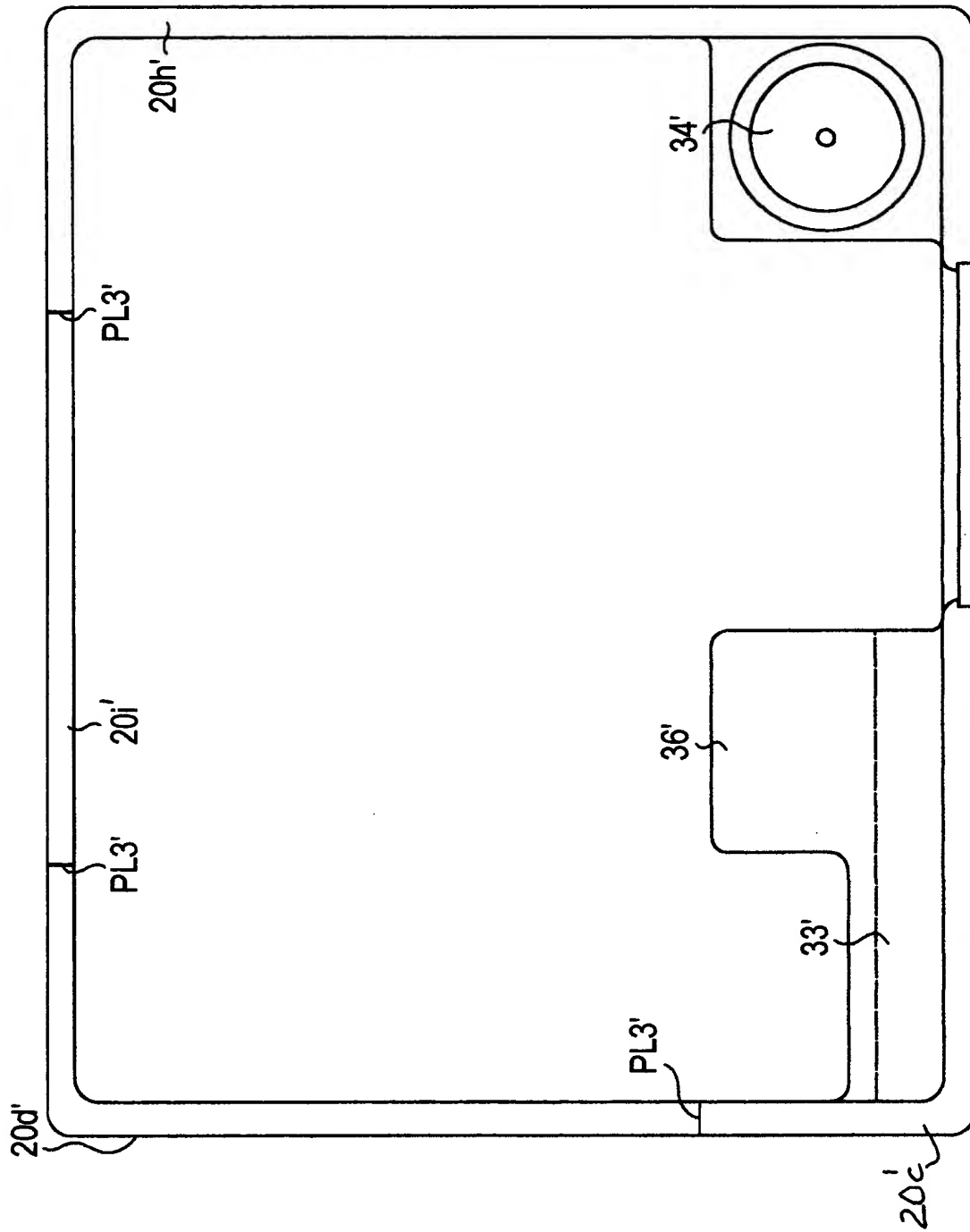


FIG. 8



FIG. 9 is a schematic diagram of a device 100.

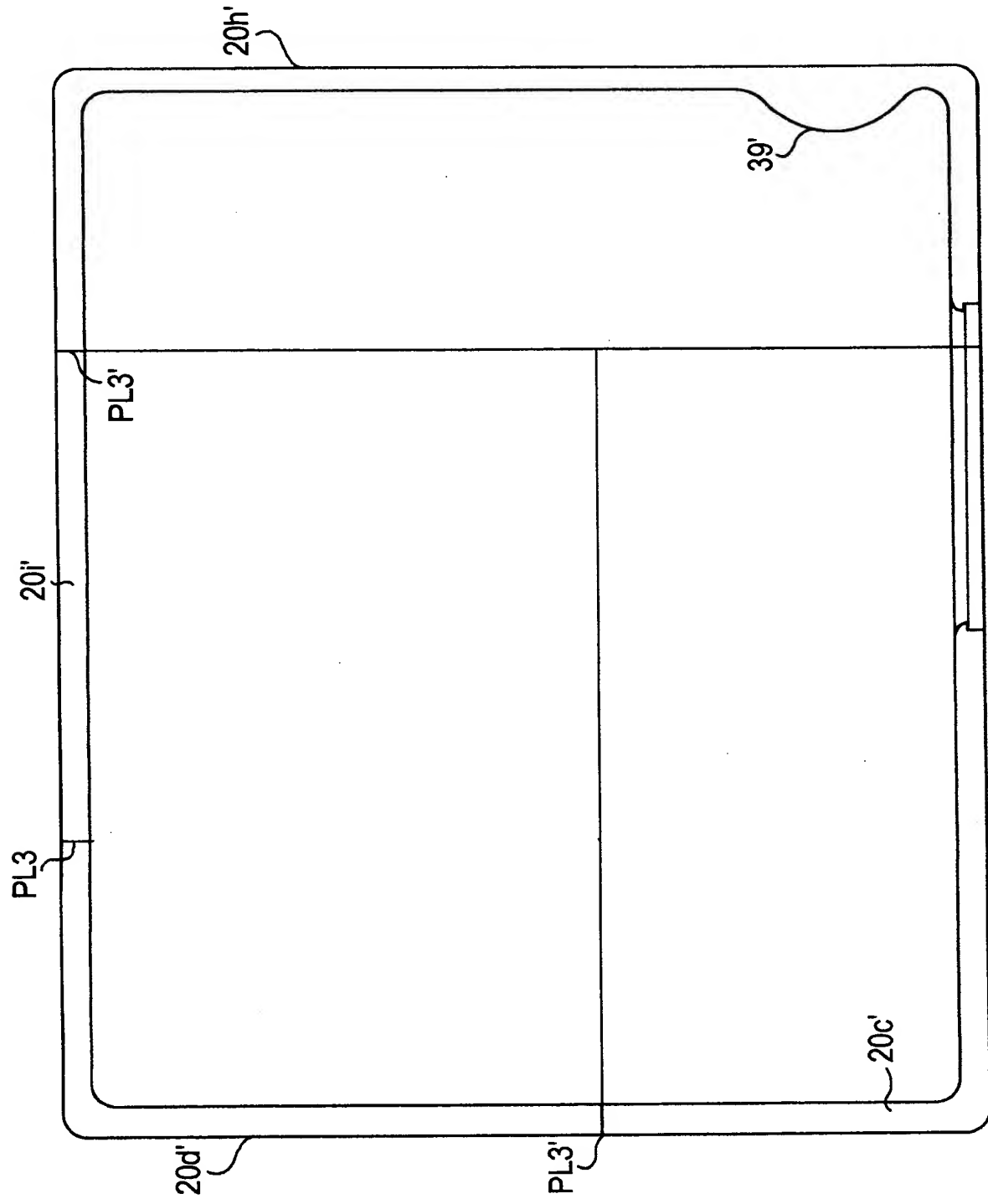


FIG. 9

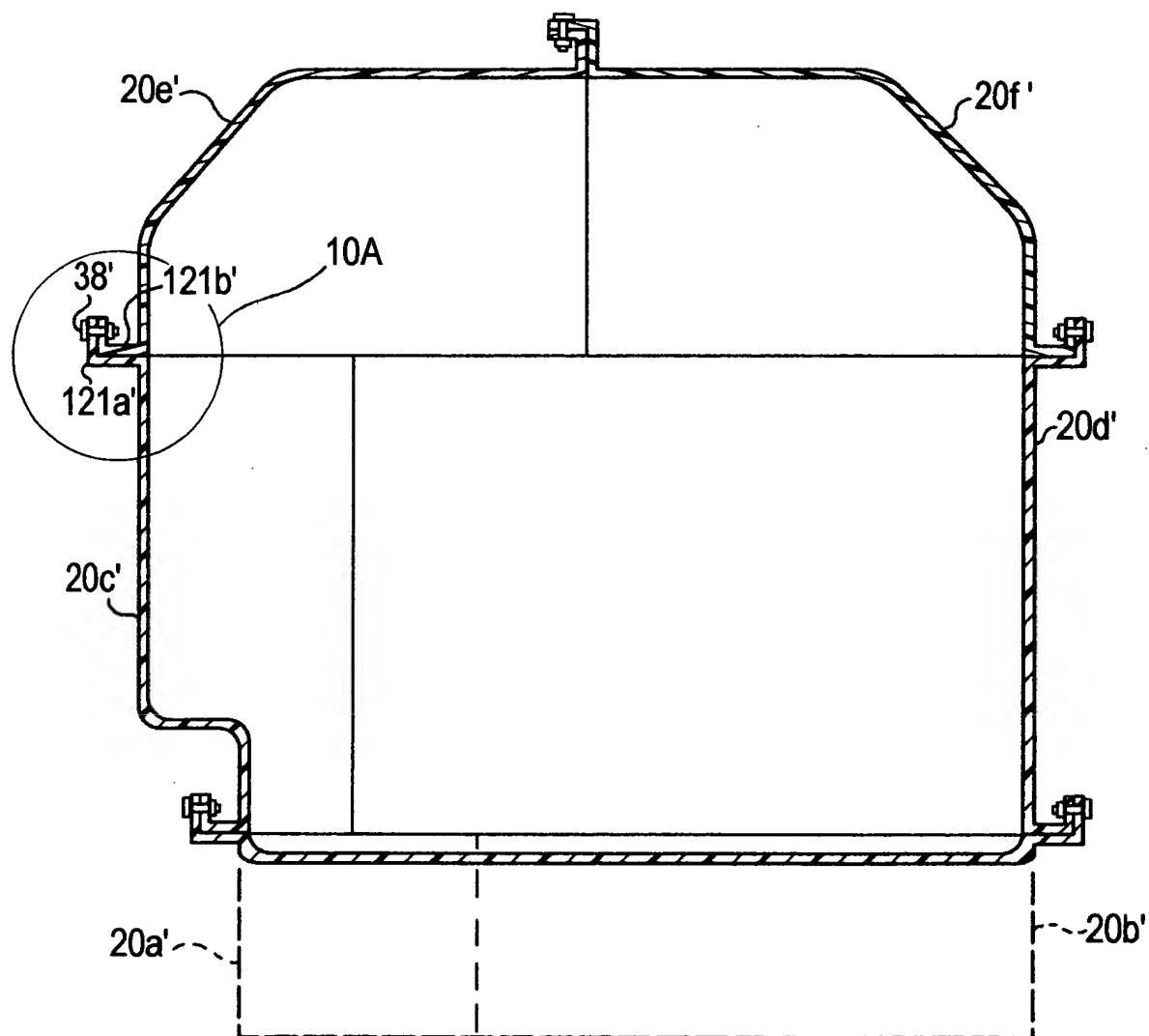


FIG. 10

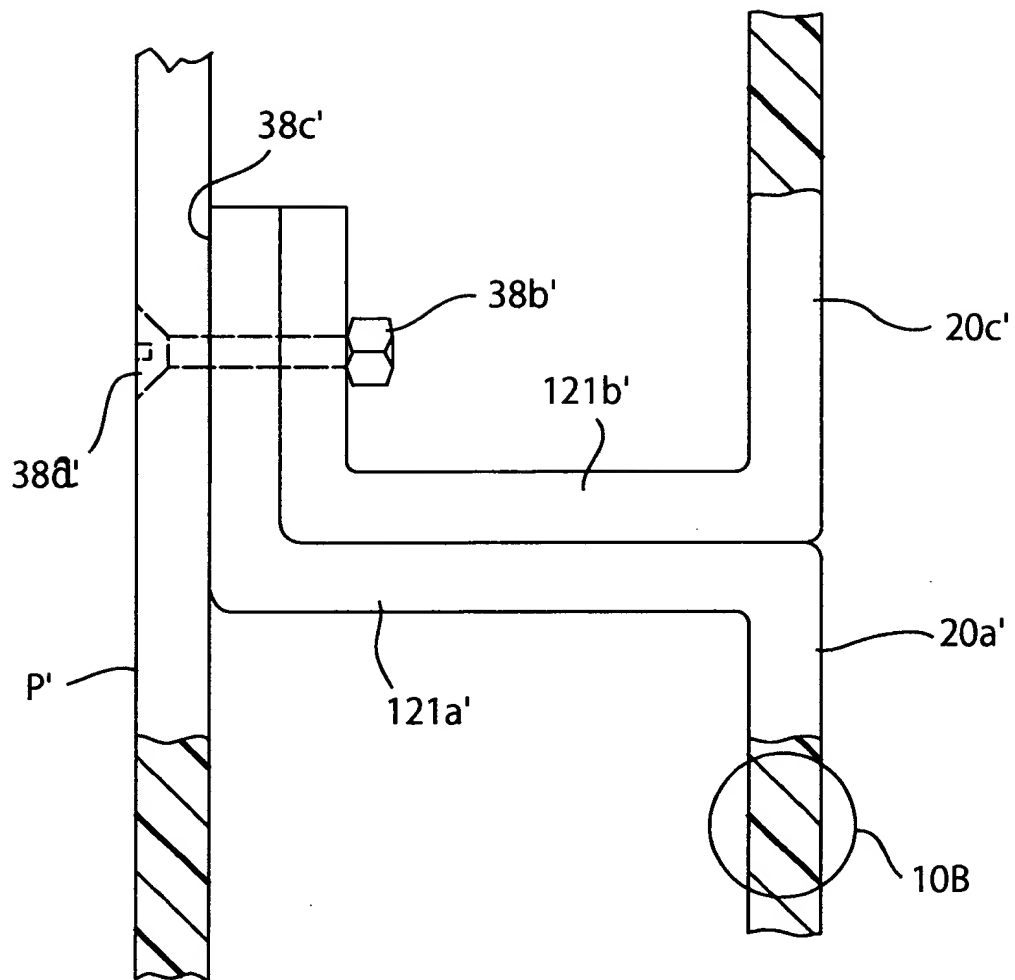


FIG. 10A

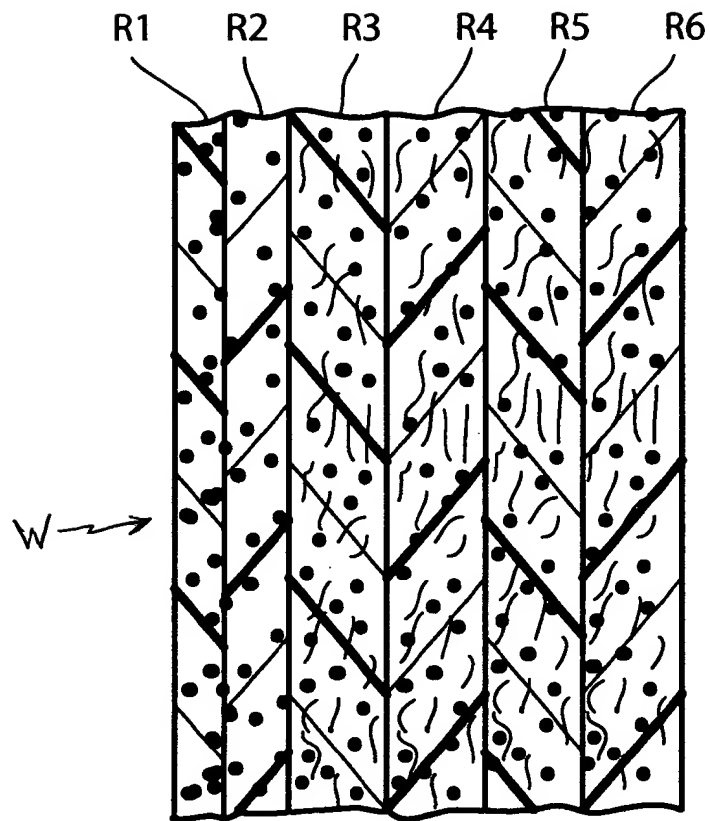


FIG. 10B

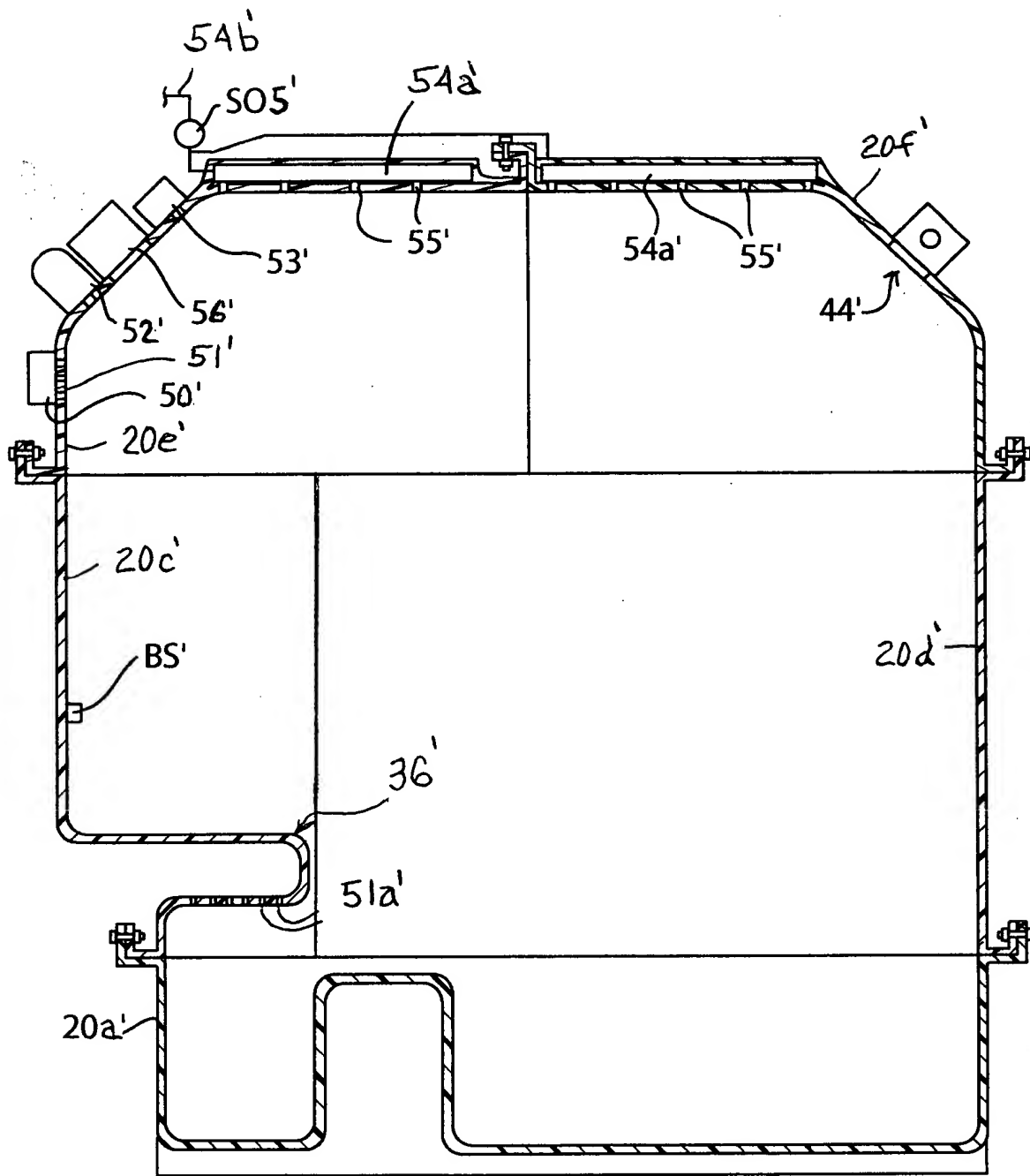


FIG. 11

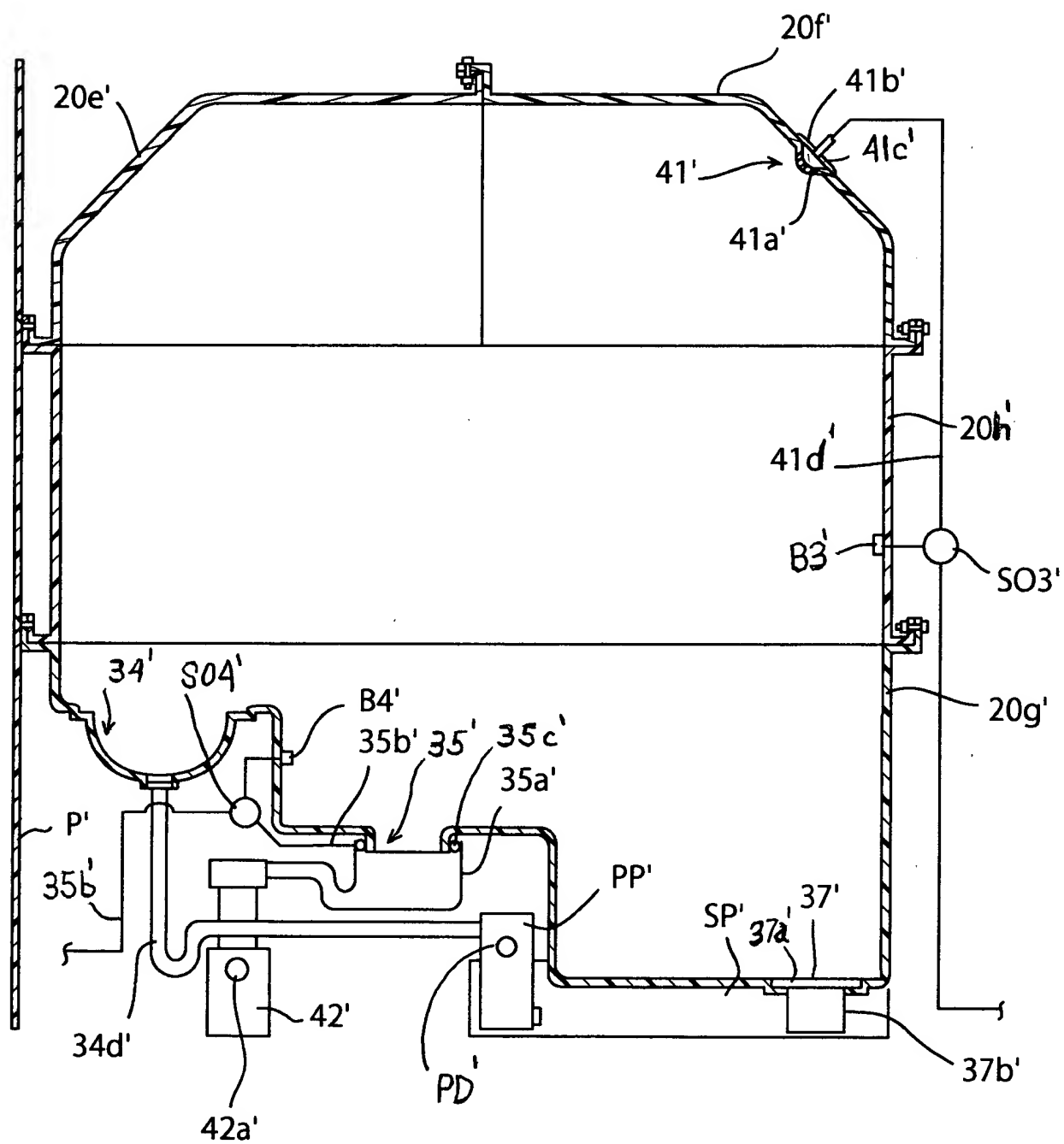
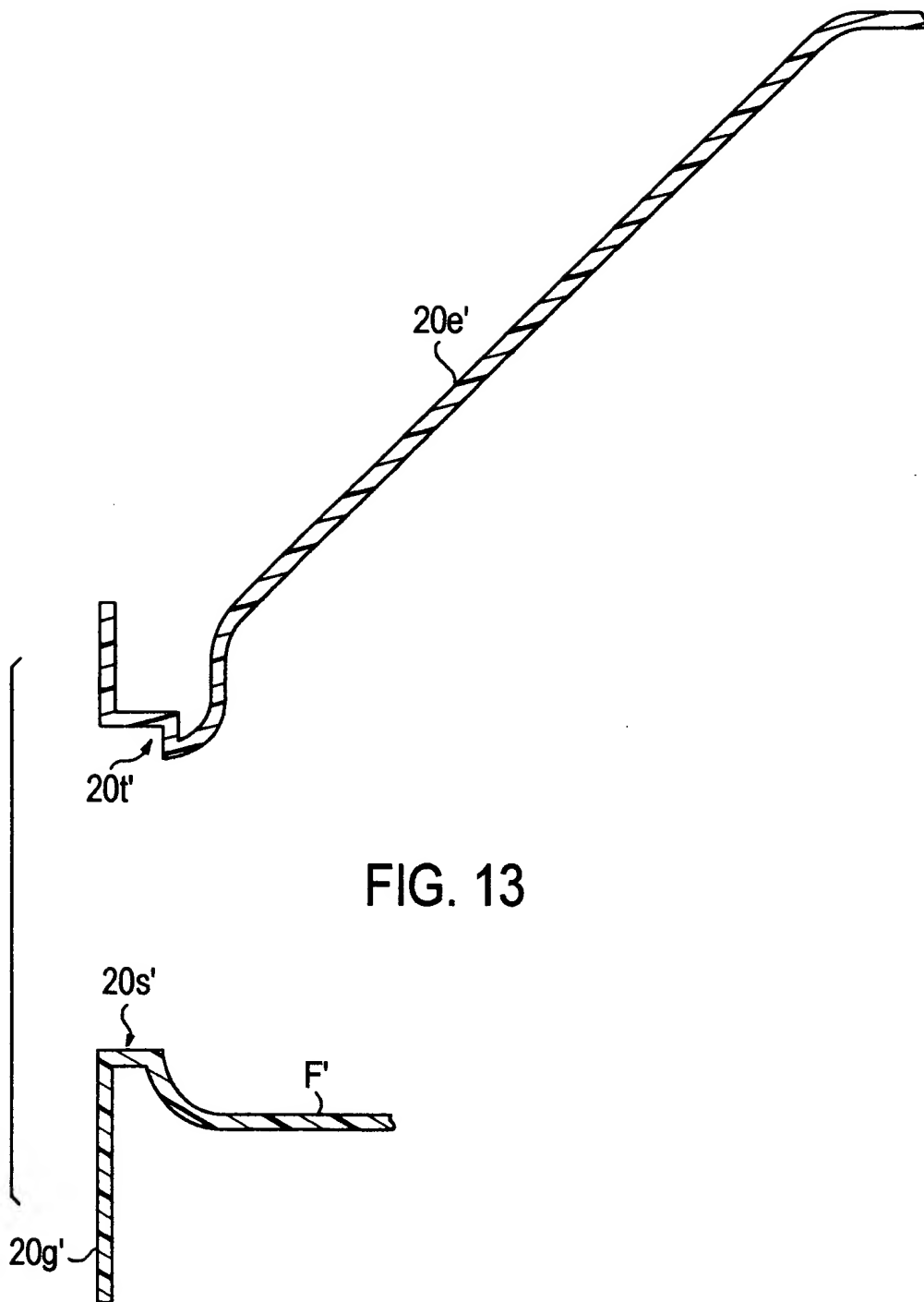


FIG. 12



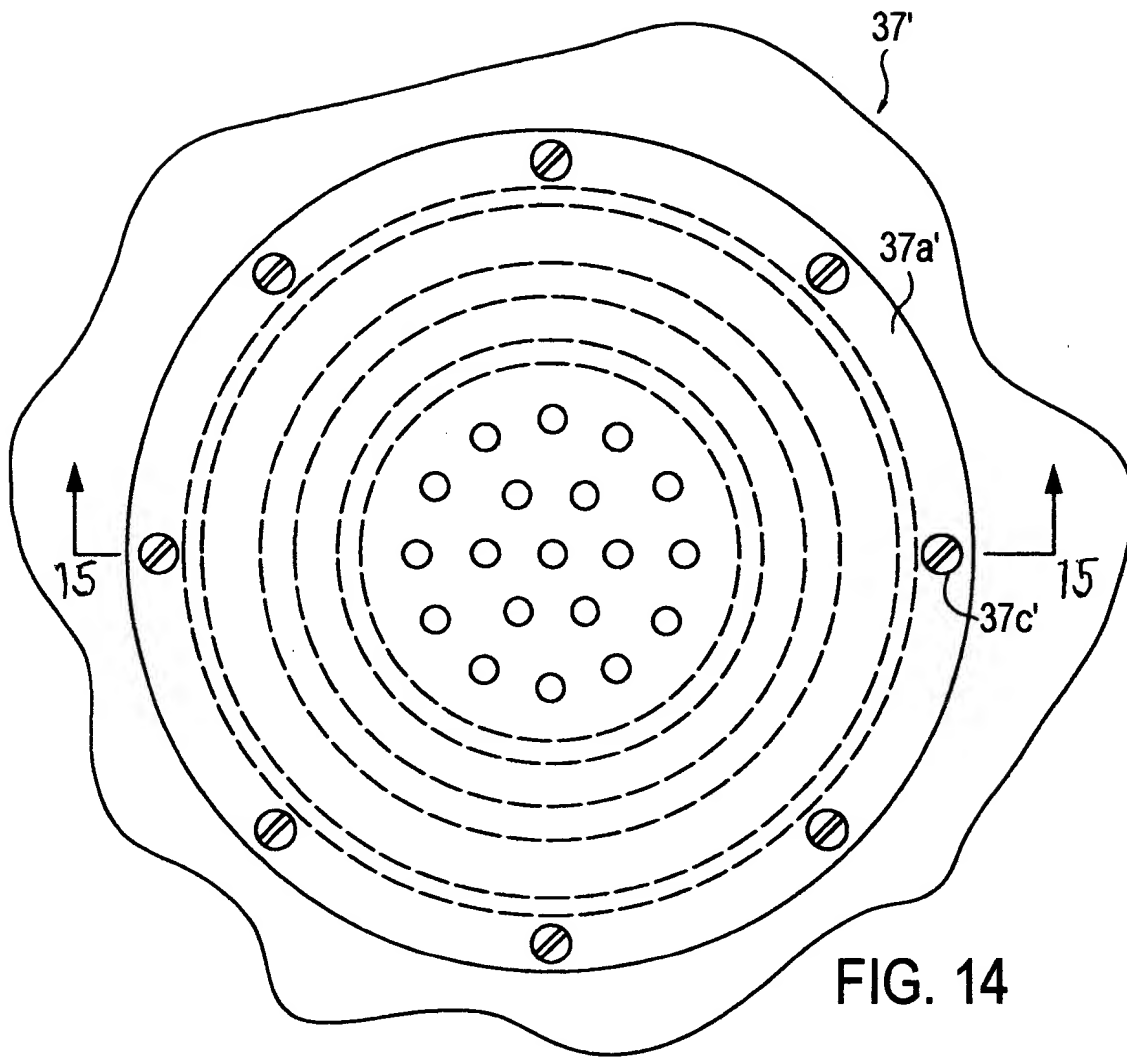


FIG. 14

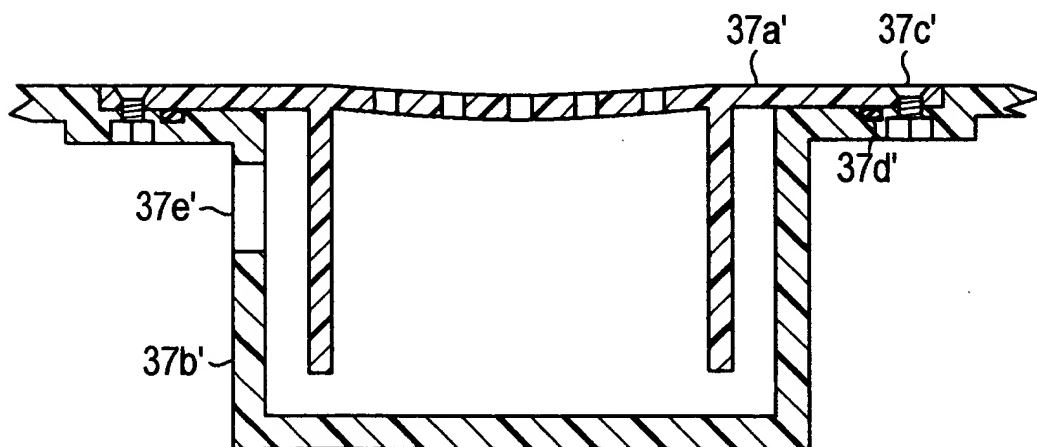


FIG. 15



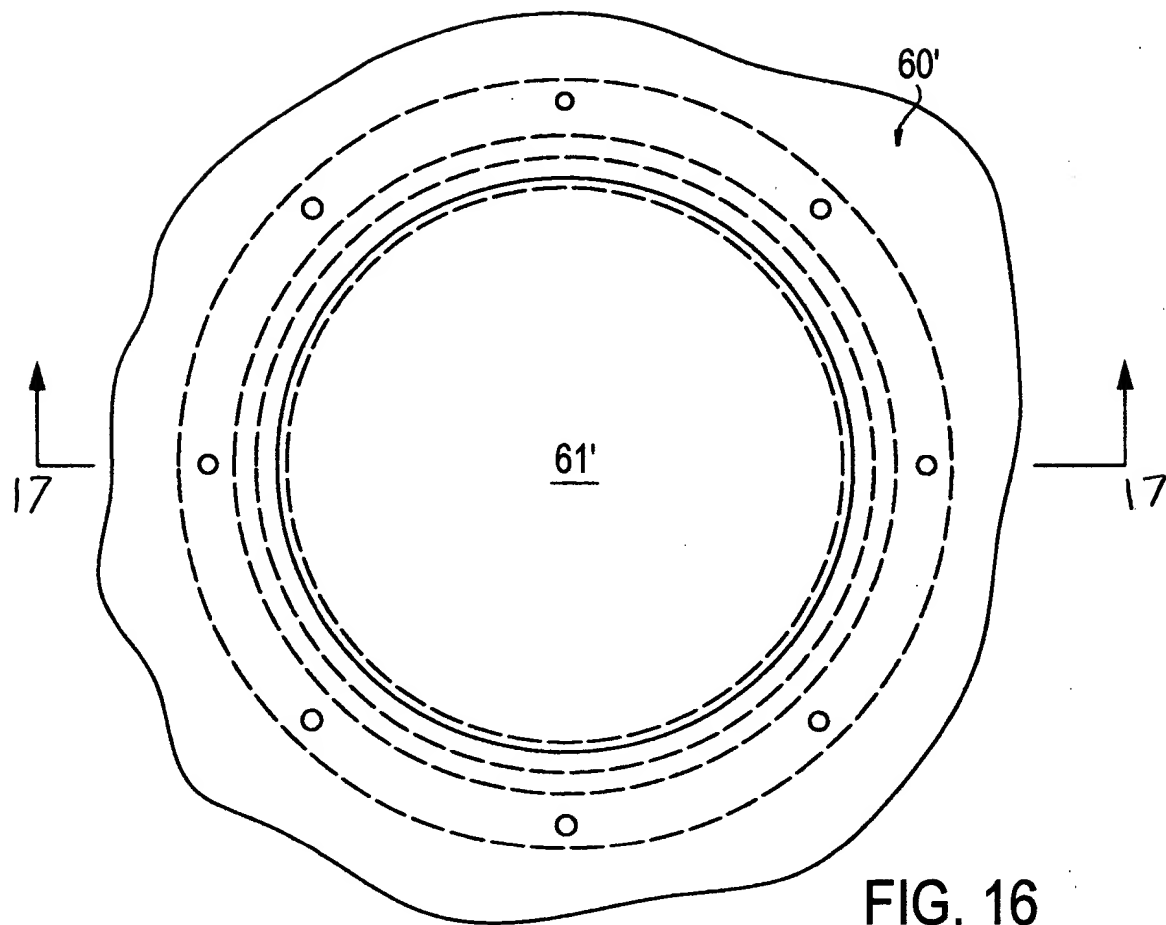


FIG. 16

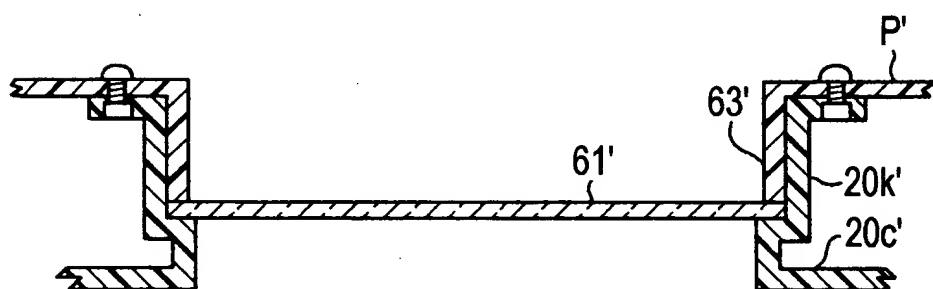


FIG. 17

FIG. 18

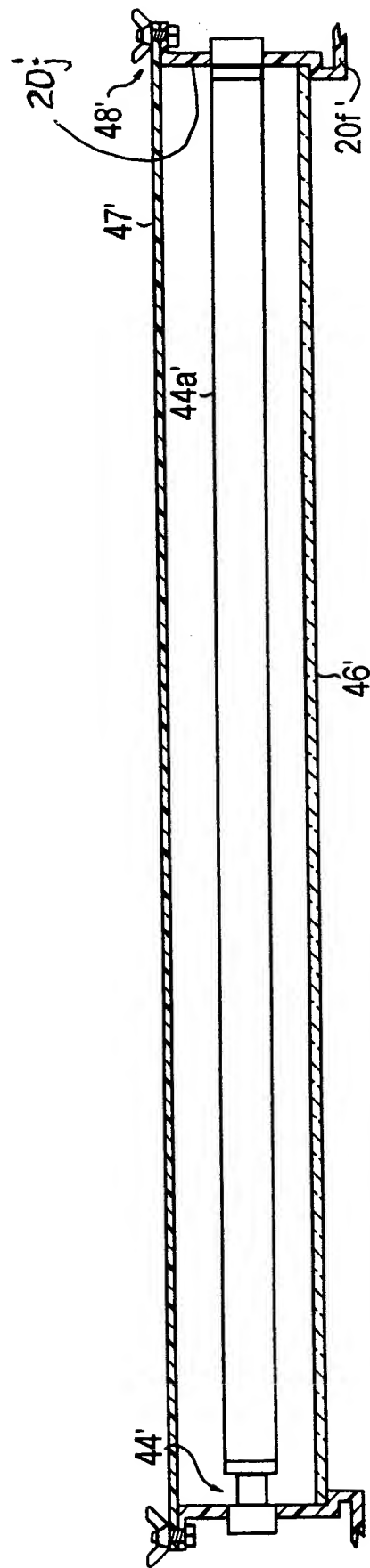


FIG. 18

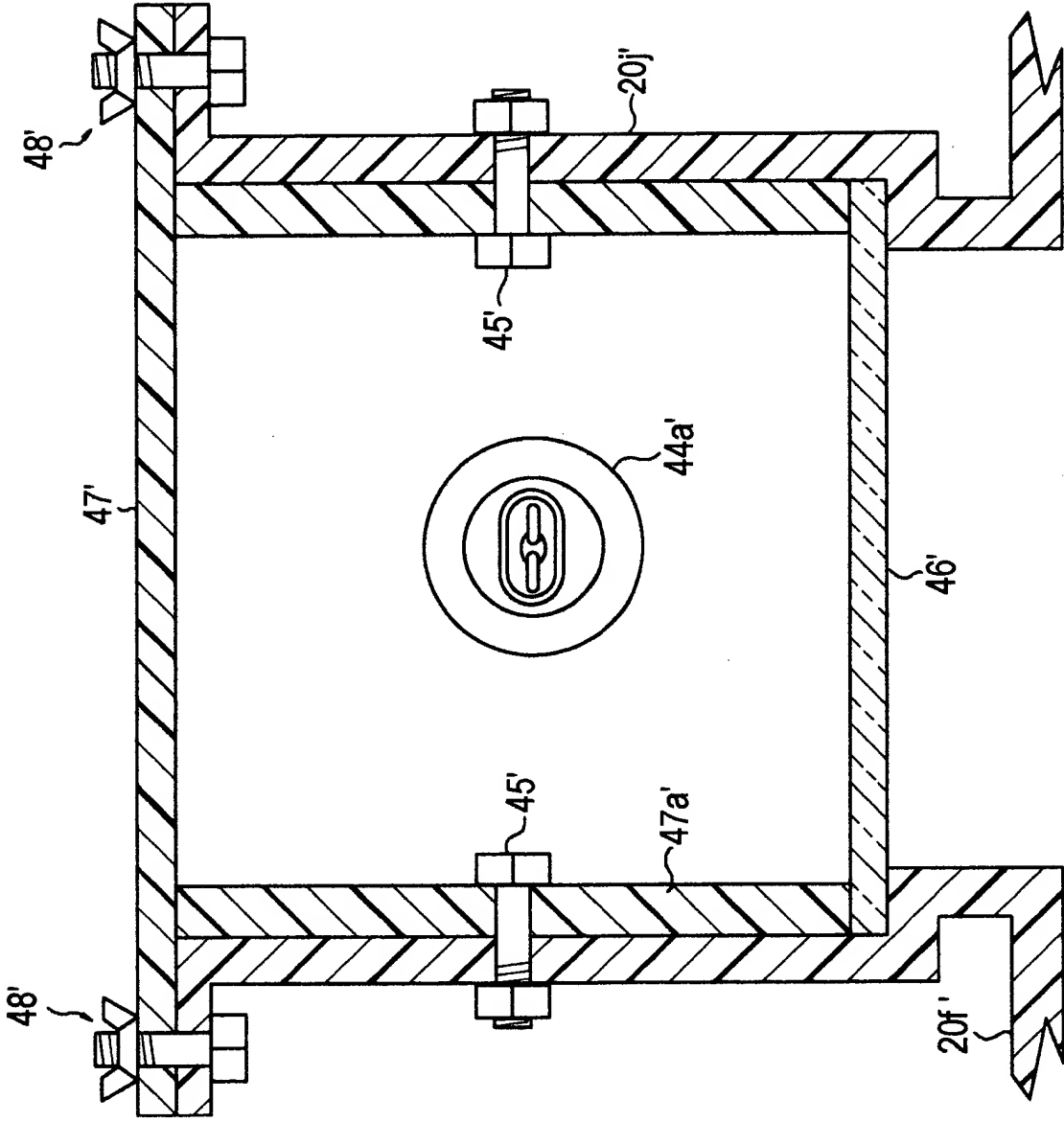


FIG. 19

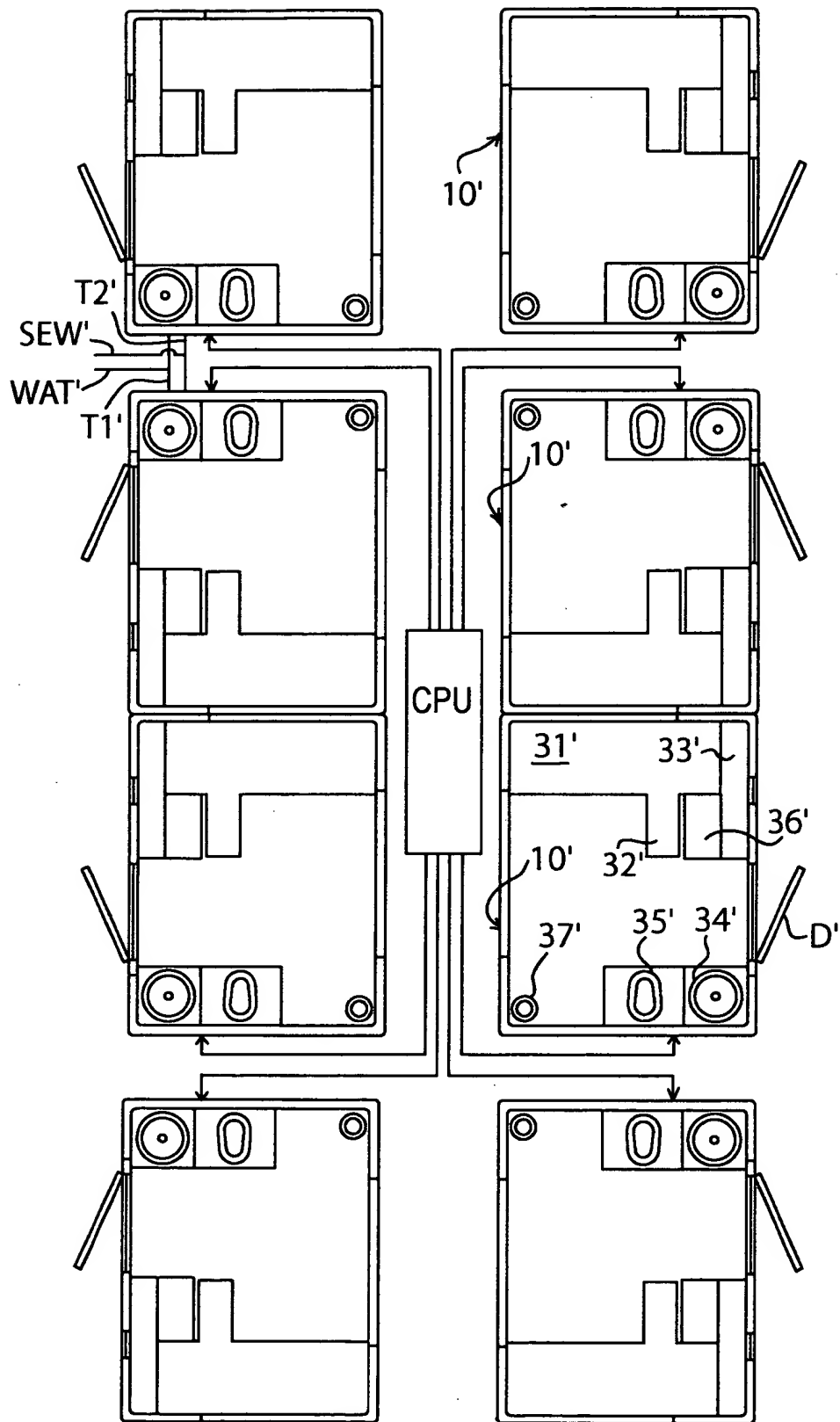


FIG. 20

FIG. 21

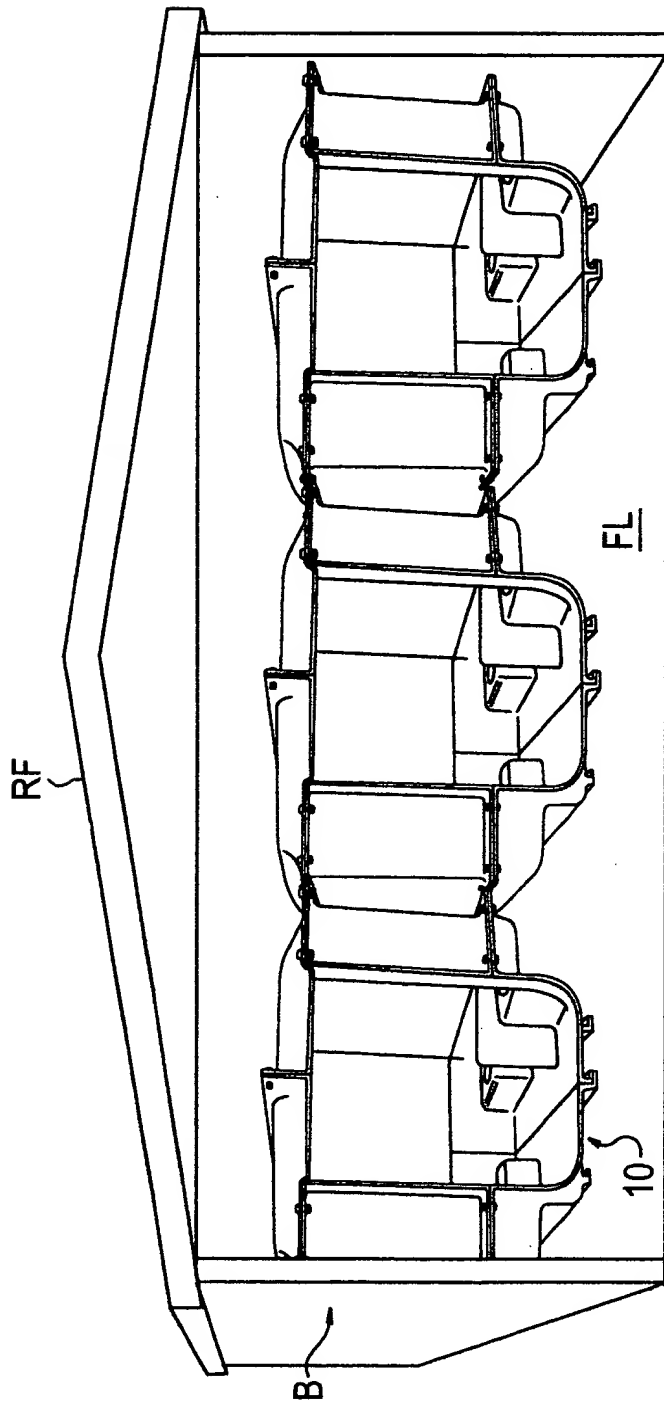


FIG. 21